

simultaneously couple bit lines having a same column address of each bank and carrying data, amplified by said bit line sense amplifiers, to local data bus lines;

input/output sense amplifiers for amplifying data on said local data bus lines, respectively;

a transmission gate unit for controlling transmission of data from said input/output sense amplifiers to global read data bus lines; and

an input/output comparing unit for compressing data from said input/output sense amplifiers prior to said transmission thereof to said global read data bus lines.

REMARKS

Claims 1-8 are pending in the application and are presented for reconsideration and further examination in view of the foregoing amendments and the following remarks.

In the outstanding Office Action the oath/declaration was objected to; the drawings of Figs. 4B and 5 were objected to; claim 1 was rejected under 35 U.S.C. 102(e); and claims 2-8 were objected to as being dependent on rejected base claim 1.

By this Response and Amendment a new oath/declaration is herewith submitted; a Letter to the Draftsperson is submitted herewith together with proposed corrections to the drawings of Figs. 4B, 5, and 6; the specification is amended; and claim 1 is